

FIG. 1

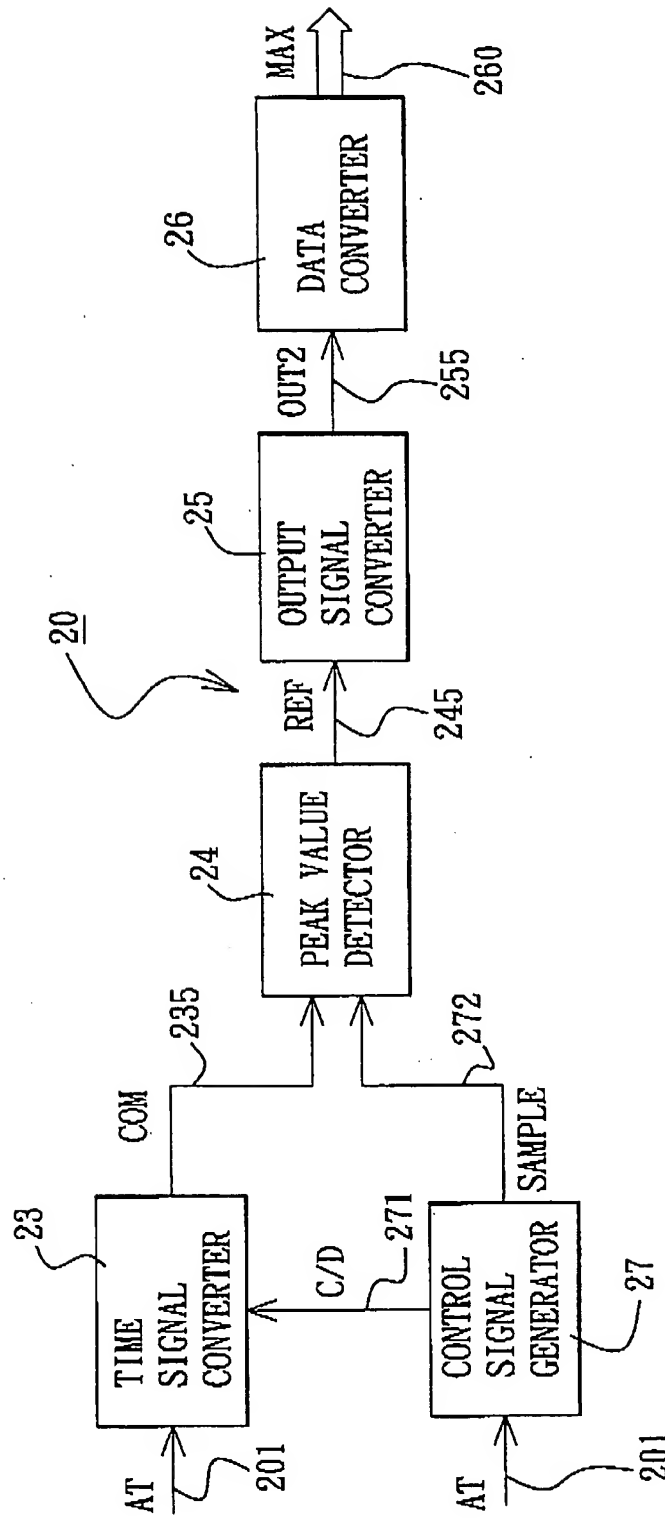


FIG. 2

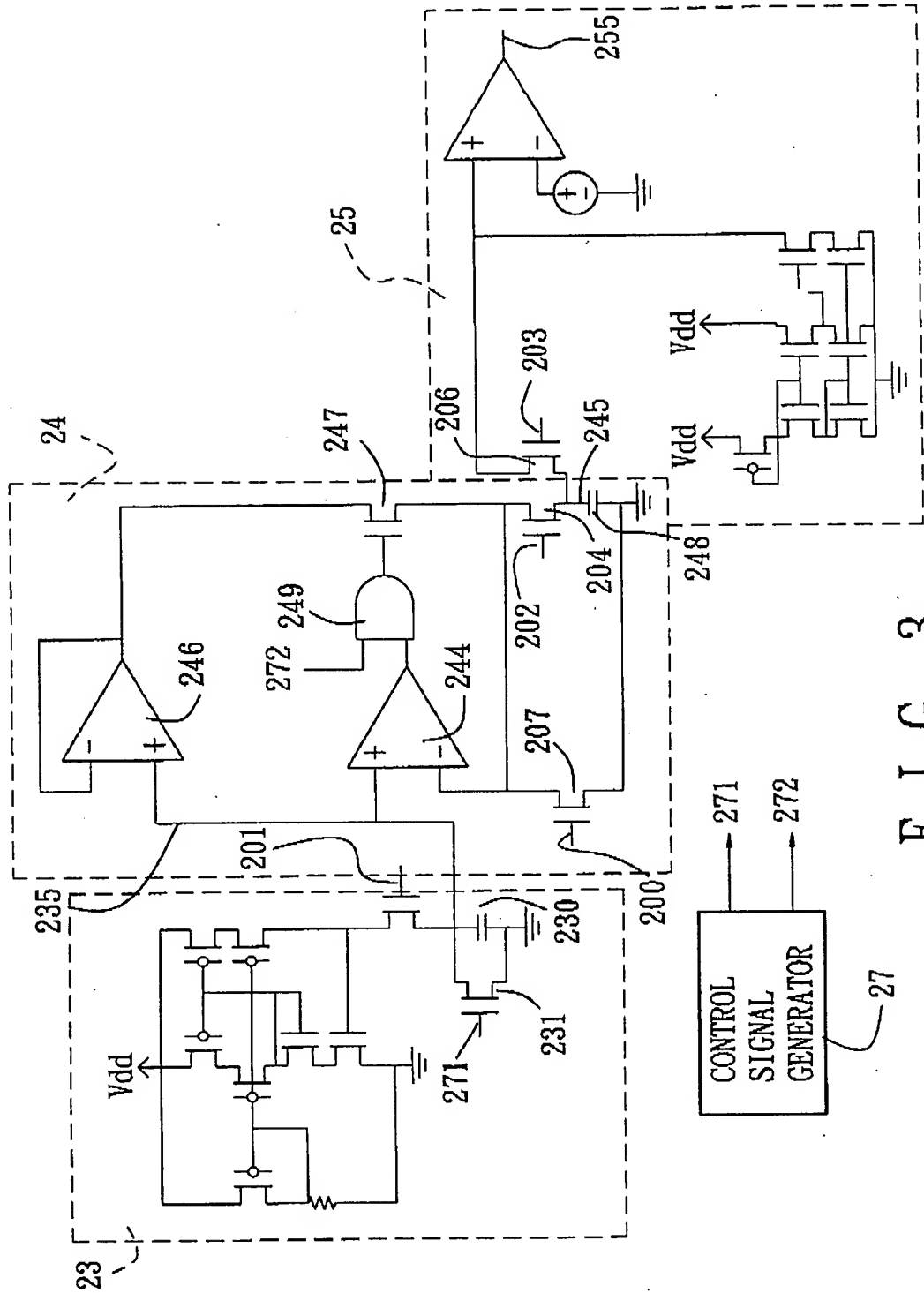


FIG. 3

Title: INTEGRATED CIRCUIT DEVICE WITH A BUILT-IN
DETECTING CIRCUIT FOR DETECTING MAXIMUM
MEMORY ACCESS...

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